

MH283 is an unipolar Hall effect sensor IC. It incorporates advanced chopper stabilization technology to provide accurate and stable magnetic switch points. The design, specifications and performance have been optimized for applications of solid state switches.

The output transistor will be switched on (BOP) in the presence of a sufficiently strong South pole magnetic field facing the marked side of the package. Similarly, the output will be switched off (BRP) in the presence of a weaker South field and remain off with "0" field. The Pull high resistor has been integrated.

The package type is in a Green version was verified by third party Lab.

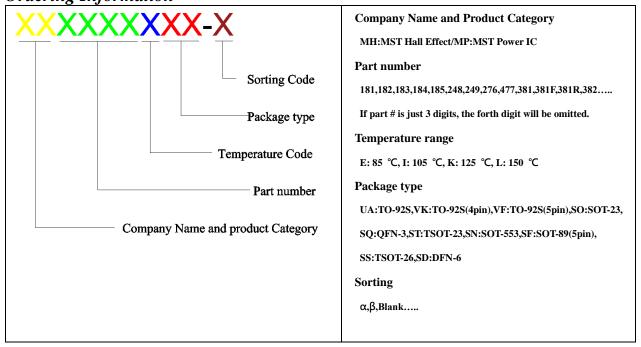
### Features and Benefits

- DMOS Hall IC Technology.
- Reverse bias protection on power supply pin.
- Solid-State Reliability.
- Chopper stabilized amplifier stage.
- Unipolar, output switches with absolute value of South pole from magnet.
- Operation down to 3.0V.
- High Sensitivity for direct reed switch replacement applications.
- 100% tested at 125°C for K Spec.
- Custom sensitivity / Temperature selection are available.
- Good ESD Protection.
- RoHS compliant 2011/65/EU and Halogen Free

### **Applications**

- Solid state switch
- Limit switch
- Current limit
- Interrupter
- Current sensing
- Magnet proximity sensor for reed switch replacement

**Ordering Information** 

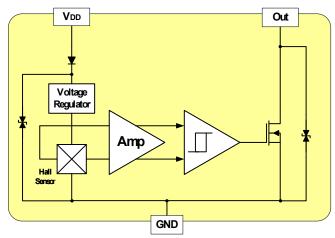




Part No.	Temperature Suffix	Package Type
MH283KUA	$K (-40^{\circ}C \text{ to} + 125^{\circ}C)$	UA (TO-92S)
MH283KSO	$K (-40^{\circ}C \text{ to} + 125^{\circ}C)$	SO (SOT-23)
MH283EUA	$E \left(-40^{\circ}C \text{ to} + 85^{\circ}C\right)$	UA (TO-92S)
MH283ESO	$E (-40^{\circ}C \text{ to} + 85^{\circ}C)$	SO (SOT-23)

KUA spec is using in industrial and automotive application. Special Hot Testing is utilized.

### Functional Diagram



Note: Static sensitive device; please observe ESD precautions. Reverse  $V_{DD}$  protection is not included. For reverse voltage protection, a  $11K\Omega$  resistor in series with  $V_{DD}$  is recommended.

**Absolute Maximum Ratings** At (Ta=25°C)

Characteristic	cs	Values	Unit	
Supply voltage, $(V_{DD})$		28	V	
Output Voltage, $(V_{OUT})$		28	V	
Reverse Voltage, $(V_{DD})$		-27	V	
Magnetic flux density		Unlimited	Gauss	
Output current, $(I_{OUT})$		50	mA	
Operating Temperature Ran	nge, "E" version	-40 to +85	°C	
(Ta)	"K" version	-40 to +125	°C	
Storage temperature range, (Ts)	)	-55 to +150	°C	
Maximum Junction Temp,( <i>Tj</i> )		150	°C	
Thermal Resistance	$(\theta ja)$ UA / SO	206 / 543	°C/W	
Thermal Resistance	(θjc) UA / SO	148 / 410	°C/W	
Package Power Dissipation, $(P_D)$ UA / SO		606 / 230	mW	

Note: Do not apply reverse voltage to  $V_{DD}$  and  $V_{OUT}$  Pin, It may be caused for Miss function or damaged device.

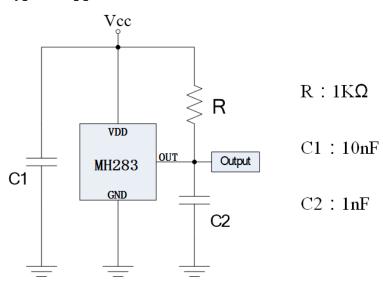


## **Electrical Specifications**

DC Operating Parameters TA=+25°C, VDD=12V (Unless otherwise specified)

Parameters	<b>Test Conditions</b>	Min	Тур	Max	Units
Supply Voltage,(VDD)	Operating	3.0		24.0	V
Supply Current,(IDD)	B <bop< td=""><td></td><td>2.5</td><td>5.0</td><td>mA</td></bop<>		2.5	5.0	mA
Output Saturation Voltage, ( <i>Vsat</i> )	Iout = 20 mA, B>BOP			500.0	mV
Output Leakage Current, (Ioff)	IOFF B <brp, vout="20V&lt;/td"><td></td><td></td><td>10.0</td><td>uA</td></brp,>			10.0	uA
Output Rise Time, (TR)	RL=1k $\Omega$ , CL =20pF		0.04	0.45	uS
Output Fall Time, (TF)	RL=820Ω; CL =20pF		0.18	0.45	uS
Electro-Static Discharge	HBM	4			KV
Operate Point (B <sub>OP</sub> )		90		150	Gauss
Release Point (B <sub>RP</sub> )		40		100	Gauss
Hysteresis (B <sub>HYS</sub> )			50		Gauss

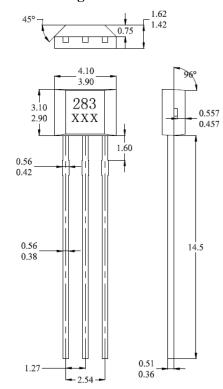
## Typical application circuit





### Sensor Location, Package Dimension and Marking

### **UA Package**



### **NOTES:**

- 1. Controlling dimension: mm
- 2.Leads must be free of flash and plating voids
- 3.Do not bend leads within 1 mm of lead to package interface.

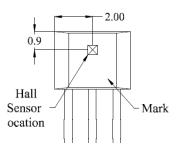
Output

4.PINOUT:

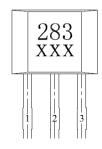
Pin 3

Pin 1 VCC Pin 2 GND

### **Hall Chip location**

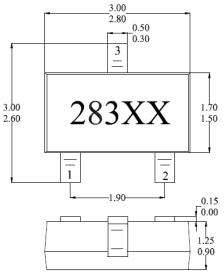


# Output Pin Assignment (Top view)



### **SO Package**

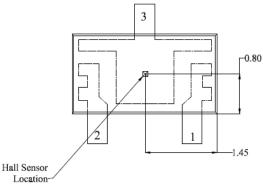
### (Top View)



# 0.55 0.35 0.436 0.434

# Н

# Hall Plate Chip Location (Bottom view)



### (For reference only)Land Pattern

# 1,5

### **NOTES:**

- 1.PINOUT (See Top View at left:)
  - Pin 1 V<sub>CC</sub>
  - Pin 2 Output
  - Pin 3 GND
- 2.Controlling dimension: mm
- 3.Lead thickness after solder plating will be  $0.254 \mathrm{mm}$  maximum