

The MH483IVK is a magnetic field sensor for accurate measurements in harsh environments. It combines an integrated Hall-effect sensor with on-chip signal conditioning electronics to achieve an unsurpassed accuracy and dynamic range.

In a CMOS integrated Hall IC sensitivity varies with processing parameters of silicon. For an accurate sensitivity this parameter needs to be trimmed and coarse and fine trim bits are available. The temperature coefficient of the sensitivity needs to be trimmed as well to achieve 200ppm/ $^{\circ}\text{C}$ .

The on-chip memory is EEPROM that allows up to 1,000 write/erase cycles at factory trimming or in a customer application. Programming can be done using a normal 5V supply; high programming voltage is generated on-chip.

## **Features and Benefits**

- Fully integrated Hall-effect based Magnetic Field sensor
- No internal magnetic concentrator
- User gain and gain tc trimming possible
- Internal Vcc/2 reference
- Fast response time
- RoHS compliant 2011/65/EU and Halogen Free

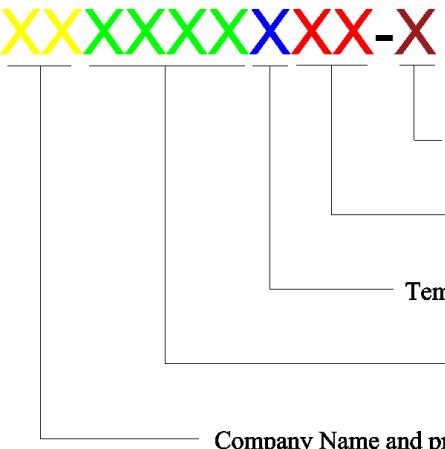
## **Applications**

- BLDC Motor Current Sensing
- Over-current protection
- Ground-Fault detection
- Joystick
- Rotary Controls
- Linear / Rotary Position Sensors
- Solar / Wind power junction boxes
- DC/AC Current Sensor Application domain

## **Major markets**

- Industrial / agricultural motor controllers
- Electric vehicles (including forklifts, golf carts, trains, IC process is automotive. Assembly, FT, standards, etc...)
- Power conversion / battery charging Current sensor photos

### *Ordering Information*

 <b>Company Name and product Category</b>	<b>Company Name and Product Category</b> <b>MH:MST Hall Effect/MP:MST Power IC</b>  <b>Part number</b> <b>181,D182,183,184,185,248,477,D381,D381F,381R,D382.....</b>  <b>If part # is just 3 digits, the forth digit will be omitted.</b>  <b>Temperature range</b> <b>E: 85 °C, I: 105 °C, K: 125 °C, L: 150 °C</b>  <b>Package type</b> <b>UA:TO-92S,VK:TO-92S(4pin),VF:TO-92S(5pin),SO:SOT-23, SQ:QFN-3,ST:TSOT-23,SN:SOT-553,SF:SOT-89(5pin), SS:TSOT-26,SD:DFN-6,SG:SOT-89(3pin)</b>  <b>Sorting</b> <b><math>\alpha, \beta</math>,Blank.....</b>
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Part No.	Temperature Suffix	Package Type
MH483IVK	I (-40°C to + 105°C)	VK (TO-92S 4PIN)

### *Absolute Maximum Ratings*

nr	PARAMETER	Symbol	Test Conditions	MH483IVK
Ar1	Junction temperature	TJ		<165 °C
Ar2	Supply voltage(5V mode, operation)	Vcc_5V		8V
Ar3	Supply voltage(5V mode, programming method 1)	Vcc <sub>prog1</sub> _5V		5.5 V
Ar4	Supply voltage(5V mode, programming method 2)	Vcc <sub>prog2</sub> _5V		11.0V
Ar5	Electrostatic discharge		JESD22-A114	4 kV
Ar6	Latch up		JESD78A	

### *Pinout*

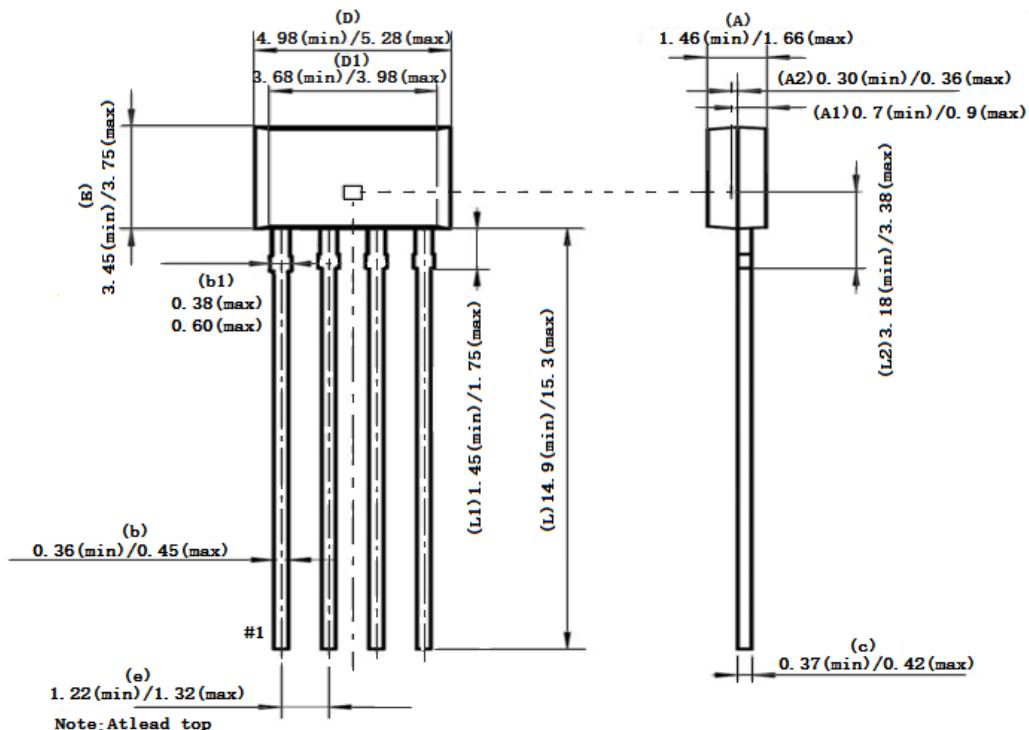
PAD	Description	SIP 4 pin	Min	Max
			(V)	(V)
Prog	N.C /Program	1	-0.3	5.5
V <sub>OUT</sub>	Analog output voltage/Program	2	-0.3	5.5
GND	Ground connection	3		0
V <sub>CC</sub>	Supply connection	4	-0.3	5.5

**Electrical Specifications (5V operation)**

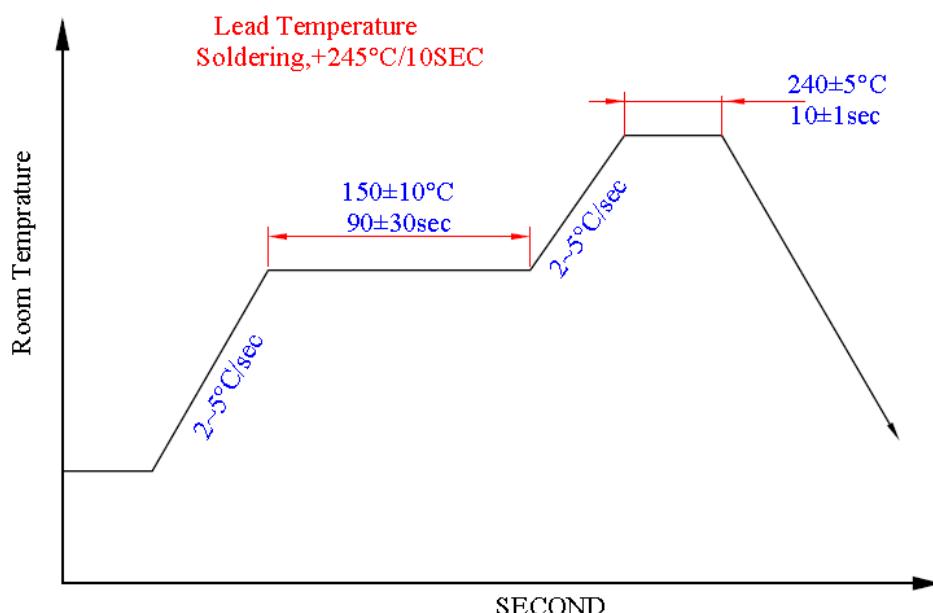
PARAMETER	Symbol	Test Conditions	Specification			unit
			Min	Typ	Max	
Storage temperature	T <sub>S</sub>		-40		125	°C
Operating temperature	T <sub>O</sub>	IC functional range 5V	-40		105	°C
Supply voltage	V <sub>CC</sub>	IC functional range	4.5	5	5.5	V
Supply current	I <sub>CC</sub>	V <sub>CC</sub> = 5.0 V, R <sub>L</sub> = 10 kΩ	10	13	15	mA
Sensitivity program range	S	Over full range of B, T <sub>A</sub> = 25 °C. Program range.	9		250	V/T
Rise time	t <sub>RISE</sub>	T <sub>A</sub> = 25 °C, di/dt=F.S./μs, input signal rise time <1 μs. Measured 10%-90% levels.		10		μs
Frequency bandwidth	BW	-3 dB, T <sub>A</sub> = 25 °C		35		kHz
Temperature coefficient	T <sub>CVO</sub>	At 25 °C. calibrated IC, without TC <sub>OF</sub> . Program options	-250, 0, 250, 500, 750, 1000			ppm/°C
Temperature coefficient variation of Sensitivity	δTCvo	Over full range of B <sub>M</sub> and T <sub>A</sub> , calibrated IC, without TC <sub>OF</sub> .	-200		200	ppm/°C
Noise-high gain	V <sub>NOISE-high gain</sub>	T <sub>A</sub> = 25 °C, S=125 V/T 1 kHz-100k Hz		10		mV <sub>rms</sub>
Nonlinearity error	E <sub>LIN</sub>	Over full range of B <sub>M</sub> , T <sub>A</sub> = 25 °C	-0.5		0.5	%
Saturation voltage	V <sub>OMAX</sub>	V <sub>CC</sub> =5 V, R <sub>L</sub> =10 kΩ	V <sub>CC</sub>			V
	V <sub>OMIN</sub>		-0.15		0.15	V
Electrical offset voltage	V <sub>OF</sub>	B <sub>M</sub> = 0 μT, S=125 V/T, V <sub>OUT</sub> – V <sub>cc</sub> /2	-10		10	mV
Offset Temperature characteristic	T <sub>CVOF</sub>	B <sub>M</sub> = 0 μT, S=125 V/T, V <sub>OUT</sub> – V <sub>cc</sub> /2	-0.075		0.075	mV/°C
Total output error (including all offsets)	E <sub>TOT</sub>	Over full range of B <sub>M</sub> , T <sub>A</sub> = 25 °C, calibrated IC.	-0.5		0.5	%
		Over full range of B <sub>M</sub> and T <sub>A</sub> , calibrated IC.	-1.5		1.5	%
Output current	I <sub>O</sub>	Maximum output current	5			mA
Output load resistance	R <sub>L</sub>	Minimum load resistance	2			kΩ
Capacitive load	C <sub>L</sub>	Maximum load capacitance			100	nF

### **Sensor Location, Package Dimension and Marking**

#### **VK Package**



### **IR reflow curve**

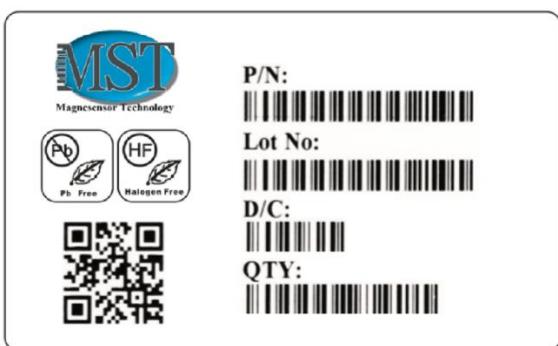


### **VK Soldering Condition**

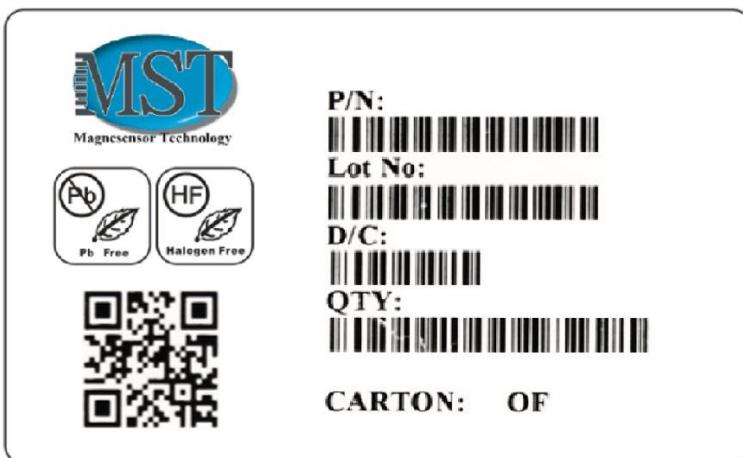
**Packing specification:**

TO-94	Weight
1000pcs/Bag	0.16kg
10 Bags/Box	1.82kg
10 Boxes/Carton	18.98kg
5 Boxes/Carton	9.63kg
4 Boxes/Carton	7.79kg

**VK Package Inner box label :** Size: 5cm\*8cm



**VK Carton label :** Size: 6 cm \* 9cm



**Combine:**

When combine lot, one reel could have two D/C and no more than two DC. One carton could have two devices, no more than two;