

MH254ESN Hall-effect sensor is a temperature stable, stress-resistant, Low Tolerance of sensitivity micro-power switch. Superior high-temperature performance is made possible through a dynamic offset cancellation that utilizes chopper-stabilization. This method reduces the offset voltage normally caused by device over molding, temperature dependencies, and thermal stress.

MH254ESN is special made for low operation voltage, 1.7V, to active the chip which is includes the following on a single silicon chip: voltage regulator, Hall voltage generator, small-signal amplifier, chopper stabilization, Schmitt trigger, CMOS output driver. Advanced CMOS wafer fabrication processing is used to take advantage of low-voltage requirements, component matching, very low input-offset errors, and small component geometries. This device requires the presence of unipolar magnetic fields for operation.

The package type is in a Halogen Free version has been verified by third party Lab.

#### Features and Benefits

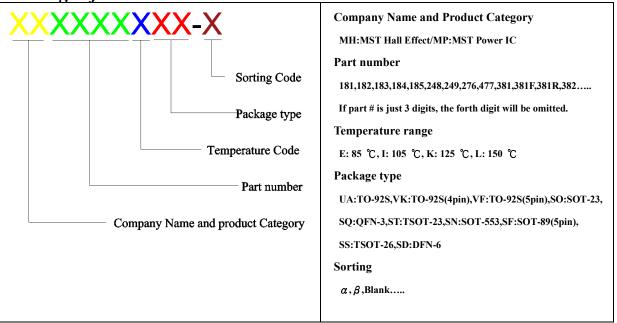
- CMOS Hall IC Technology
- Strong RF noise protection
- 1.7 to 5.5V for battery-powered applications
- Operation down to 1.7V, Unipolar Hall Switch Micro power consumption
- High Sensitivity for reed switch replacement applications
- Low sensitivity drift in crossing of Temp. range
- Ultra Low power consumption at 5uA (Avg)
- High ESD Protection, HBM  $> \pm 4$ KV(min)
- Totem-pole output
- RoHS compliant 2011/65/EU and Halogen Free

#### **Applications**

- Solid state switch
- Handheld Wireless Handset Awake Switch (Flip Cell/PHS Phone/Note Book/Flip Video Set)
- Magnet proximity sensor for reed switch replacement in low duty cycle applications
- Water Meter
- PDA
- PDVD
- NB
- Pad PC



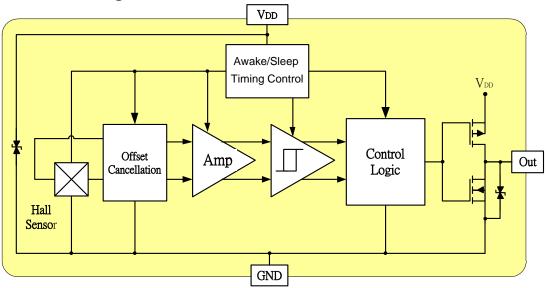
**Ordering Information** 



Part No.	Temperature Suffix	Package Type	
MH254ESN	E $(-40^{\circ}\text{C to} + 85^{\circ}\text{C})$	SN (SOT-553)	

Custom sensitivity selection is available by MST sorting technology

### Functional Diagram



Note: Static sensitive device; please observe ESD precautions. Reverse  $V_{DD}$  protection is not included. For reverse voltage protection, a  $100\Omega$  resistor in series with  $V_{DD}$  is recommended.

MH254,  $HBM > \pm 4KV$  which is verified by third party lab.



Absolute Maximum Ratings At(Ta=25 °C)

Characteristics		Values	Unit	
Supply voltage, (VDD)		6	V	
Output Voltage, (Vout)		6	V	
Reverse Voltage, (VDD) (VOUT)		-0.3	V	
Magnetic flux density		Unlimited	Gauss	
Output current, (Iour)		5	mA	
Operating temperature range, ( <i>Ta</i> )		-40 to +85	°C	
Storage temperature range, ( <i>Ts</i> )		-65 to +150	°C	
Maximum Junction Temp, ( <i>Tj</i> )		150	°C	
Thermal Resistance	$( heta_{\scriptscriptstyle JA})\mathrm{SN}$	540	°C/W	
	$( heta_{{\scriptscriptstyle JC}})$ SN	390	°C/W	
Package Power Dissipation, (PD) SN		230	mW	

**Note:** Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute maximum-rated conditions for extended periods may affect device reliability.

### Electrical Specifications

DC Operating Parameters :  $Ta=25 \, \text{°C}$ ,  $V_{DD}=1.8 V$ 

Parameters	<b>Test Conditions</b>	Min	Тур	Max	Units
Supply Voltage, $(V_{DD})$	Operating	1.7		5.5	Volts
Supply Current, ( <i>IDD</i> )	Awake State		1.5	3	mA
	Sleep State		3.5	7	μΑ
	Average		5	10	μΑ
Output Leakage	Output off			1	uA
Output High Voltage, (VOH)	IOUT=0.5mA(Source)	V <sub>DD</sub> -0.2			V
Output Low Voltage, (Vol.)	Iout=0.5mA(Sink)			0.2	V
Awake mode time, ( <i>Taw</i> )	Operating		40	80	uS
Sleep mode time, ( <i>Tsl.</i> )	Operating		40	80	mS
Duty Cycle, (D,C)			0.1		%
Electro-Static Discharge	HBM	4			KV

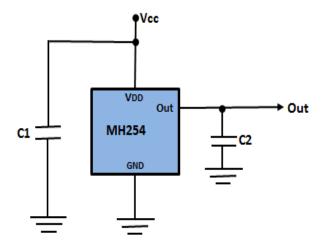
### MH254ESN Magnetic Specifications

DC Operating Parameters : Ta=25°C,  $V_{DD}=1.8V$ 

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Operating Point	$\mathrm{B}_{\mathrm{OP}}$	S pole to branded side, B > BOP, Vout On		30	50	Gauss
Release Point	$\mathrm{B}_{\mathrm{RP}}$	S pole to branded side, B < BRP, Vout Off	10	20		Gauss
Hysteresis	$\mathrm{B}_{\mathrm{HY}}$	BOPx - BRPx		10		Gauss



### Typical application circuit

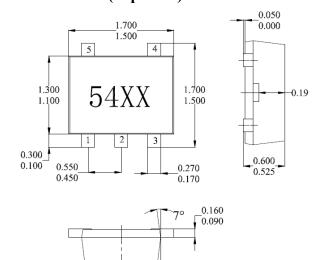


C1: 10nF

C2: 100pF

# Sensor Location, package dimension and marking SN Package (SOT-553)

### (Top View)



#### **NOTES:**

1. PINOUT (See Top View at left:)

Pin 1 NC

Pin 2 GND

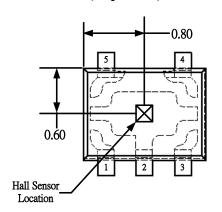
Pin 3 NC

Pin 4 VDD

Pin 5 Out

2. Controlling dimension: mm;

# Hall Plate Chip Location (Top View)



#### (For reference only)Land Pattern

